



Low-Noise LNB15 DC/DC CONVERTER

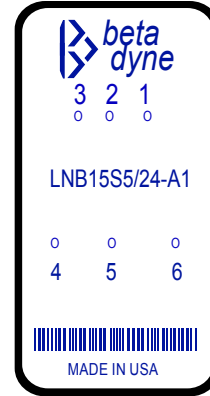
5mV Low Noise, High Efficiency 15W, 2:1 Input Range
US Patent 5,777,519

Key Features

- Wide input voltage range (2:1)
- Less than 5mV output noise
- Efficiency up to 88%
- Six-sided shielding
- Soft start
- Single/Dual output
- Short circuit protection
- Adjustable output
- 1mA off state current
- 250mV dropout linear regulators
- Dual output tracking linear regulator
- 5μS transient response
- Industry pinouts

Available upon request:

- Unbalanced output voltages for dual outputs
- 1.8, 2.5, 3.3V_{OUT}



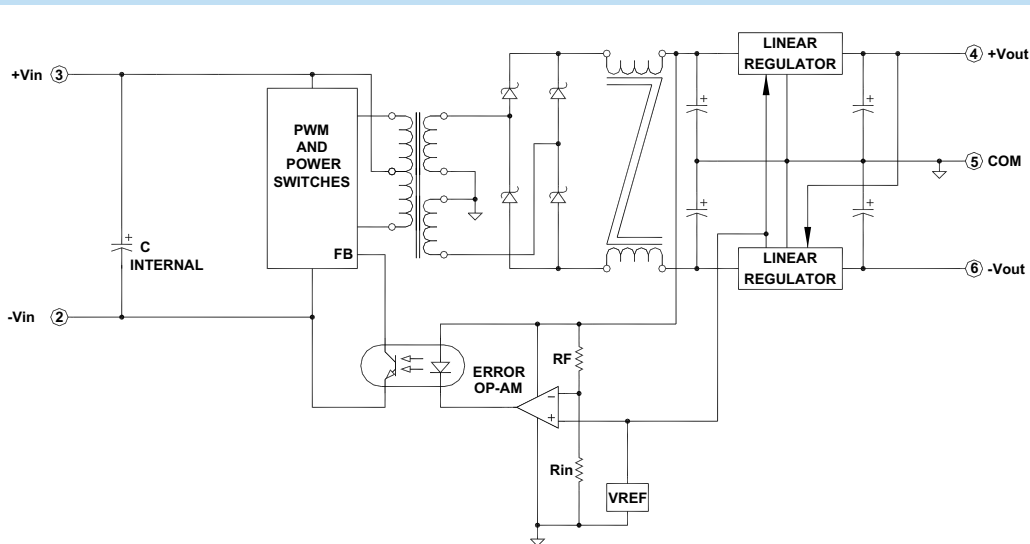
Beta Dyne is protected under various patents, including but not limited to U.S. Patent numbers: 5,777,519; 6,188,276; 6,262,901; 6,452,818; 6,473,3171.

Applications

- High-Resolution Data Converters
- Instrumentation
- Test & Measurement
- Telecom

Functional Description

The Low-Noise LNB15 series is a family of high-performance, low-noise, low-cost isolated DC/DC converters consisting of single and dual output models. The converter incorporates low switching noise techniques at its input and output sections. Low dropout linear regulators reduce the output noise to 5mV_{pp}, while a patented control circuit maintains minimum constant dropout voltage over line, load, temperature and output adjust ranges.



Typical Block Diagram of LNB15 Dual Output Converter

Electrical Specifications

INPUT SPECIFICATIONS

Unless otherwise specified, all parameters are given under typical +25°C with nominal input voltage and under full output load conditions.

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Input Voltage Range	See Model Selection Guide				
Input Filter	LC				
Reverse Polarity Input Current	External series-blocking diode			12	A
Input Surge Current (20µS Spike)				10	A
Short Circuit Current Limit			150		% I _{IN}
Undervoltage Shutdown, 12V _{in} Models		7			Vdc
Undervoltage Shutdown, 24V _{in} Models		13			Vdc
Undervoltage Shutdown, 48V _{in} Models		28			Vdc
Off State Current, 12, 24, 48V			750		µA
Remote ON/OFF Control, A2 Single Models Only					
Converter ON	Open (Open circuit voltage at Pin 1: 10V Max.)				
Converter OFF		-0.6	0	0.2	Vdc
Logic Input Reference	-Input				
Logic Compatibility	TTL Open Collector or CMOS Open Drain				

OUTPUT SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Voltage and Current Ratings	See Model Selection Guide				
Output Voltage Accuracy, Single and Dual			±1	±1.5	%
Output Voltage Adjustment, Single Only			3	±5	%
Voltage Balance, Dual ¹			±0.2	±0.5	%
Minimum Load ¹		10			% of FL
Ripple & Noise (20 MHz Bandwidth)	See Figure 4, with external capacitor		10		mV _{PP}
Ripple & Noise, 3.3V _{OUT} only (20 MHz Bandwidth)	See Figure 4, with external capacitor		20		mV _{PP}
Line Regulation, Single and Dual	Minimum V _{IN} to maximum V _{IN}		±.1		%
Load Regulation, Single	NL to FL		±.1		%
Load Regulation, Dual ²			±.1		%
Temperature Coefficient @ FL			0.02		%/°C of V _{OUT}
Transient Response Time (to within 0.5% of V _{OUT})	50% FL to FL to 50% FL, See Figure 1				µS
Short Circuit Protection	All outputs, by Hiccup technique				

GENERAL SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Efficiency	See Model Selection Guide				
Isolation Voltage (1 min.)			1500		Vdc
Isolation Resistance			10 ⁹		Ω
Isolation Capacitance			80		pF
Switching Frequency			160		kHz

ENVIRONMENTAL SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Operating Temperature, Industrial (Ambient) ³		-40		+75	°C
Operating Temperature, Extended (X)	See Ordering Guide (Please contact factory)	-55		+85	°C
Storage Temperature Range		-55		+125	°C
Maximum Operating Case Temperature				105	°C
Humidity	Up to 95% non-condensing				
Cooling	Free-air convection				
EMI/RFI	Six-sided continuous shielded metal case				
MTBF	per MIL-HNBK-217F (Ground benign, +25°C)		1×10 ⁶		hours

PHYSICAL CHARACTERISTICS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Dimensions (L×W×H)	2.00×1.00×0.445 in. (50.80×25.40×11.43mm)				
Weight	1.04 oz. (30g)				
Case Material	Coated metal				
Shielding Connection, 12, 24V _{IN}	-Input (Pin 3)				
Shielding Connection, 48V _{IN}	+Input (Pin 4)				

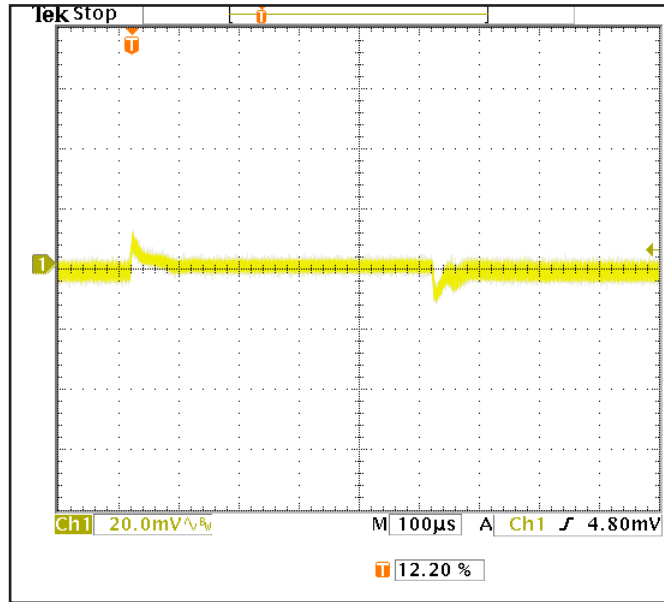


FIGURE 1. Transient response of LNB15S5/24-A1 from Full load to Half Load

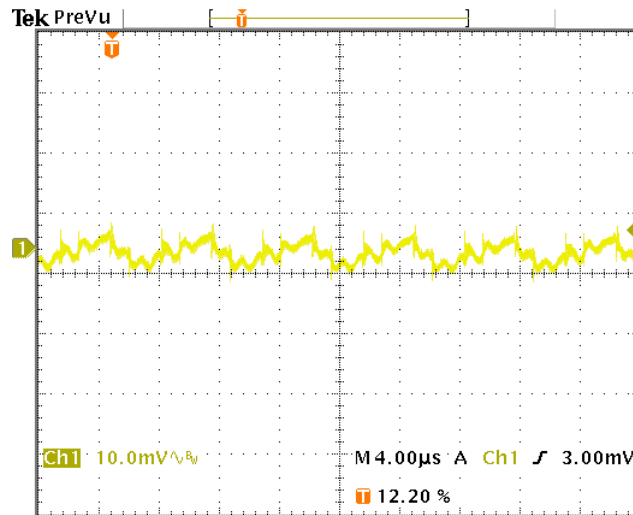


FIGURE 2. Typical output ripple of LNB15S5/24-A1 as shown in Figure 4.

OUTPUT VOLTAGE ADJUSTMENT

For single models, the output reference voltage is referenced to the output ground pin 6. To trim the output voltage high, connect a 1% resistor (0kΩ–200kΩ range) between GND(Pin 6) and V_{OUT} Adjust (Pin 5) for the singles. To trim the output voltage low, connect a 1% resistor (50kΩ–500kΩ range) between +V_{OUT} (Pin 4) and V_{OUT} Adjust (Pin 5) for the singles.

Avoid using a low resistance potentiometer or a high temperature coefficient such as wound wire.

Model Selection Guide

MODEL NUMBER	INPUT				OUTPUT		
	Voltage (Vdc)		Current (mA)		Voltage (Vdc)	Current (mA)	Efficiency Full Load (%)
	Nominal	Range	No Load	Full Load			
LNB15S2.5/12-A_	12	9-18	TBD	TBD	TBD	TBD	TBD
LNB15S3.3/12-A_	12	9-18	69	1457	+3.3	4545	86
LNB15S5/12-A_	12	9-18	87	1442	+5	3000	87
LNB15S12/12-A_	12	9-18	85	1444	+12	1250	87
LNB15S15/12-A_	12	9-18	93	1416	+15	1000	88
LNB15S1.8/24-A_*	24	18-36	60	738	1.8	8333	85
LNB15S2.5/24-A_	24	18-36	TBD	TBD	TBD	TBD	TBD
LNB15S3.3/24-A_	24	18-36	32	716	+3.3	4545	87
LNB15S5/24-A_	24	18-36	21	713	+5	3000	88
LNB15S12/24-A_	24	18-36	40	703	+12	1250	90
LNB15S15/24-A_	24	18-36	48	698	+15	1000	90
LNB15S2.5/48-A_	48	36-72	TBD	TBD	TBD	TBD	TBD
LNB15S3.3/48-A_	48	36-72	17	360	+3.3	4545	87
LNB15S5/48-A_	48	36-72	21	355	+5	3000	88
LNB15S12/48-A_	48	36-72	21	351	+12	1250	89
LNB15S15/48-A_	48	36-72	25	351	+15	1000	89
LNB15D5/12-A_	12	9-18	29	1569	±5	±1500	80
LNB15D12/12-A_	12	9-18	76	1513	±12	±625	82
LNB15D15/12-A_	12	9-18	109	1524	±15	±500	82
LNB15D5/24-A_	24	18-36	24	763	±5	±1500	82
LNB15D12/24-A_	24	18-36	50	747	±12	±625	84
LNB15D15/24-A_	24	18-36	56	743	±15	±500	84
LNB15D5/48-A_	48	36-72	10	376	±5	±1500	83
LNB15D12/48-A_	48	36-72	22	371	±12	±625	84
LNB15D15/48-A_	48	36-72	28	370	±15	±500	85

Contact factory for custom input and output voltage combinations

* Due to the 5% efficiency loss through the output linear regulator, output models of 2.5V_{out} and lower do not have output linear regulators. However, the output ripple can be reduced to less than 10mV with the external component given in Fig#4.

- ² In applications where the $-V_{OUT}$ is loaded more than $+V_{OUT}$ a minimum load is required between $+V_{OUT}$ and GND. If the load is connected between $+V_{OUT}$ and $-V_{OUT}$ no minimum load is required.
- ² For dual converters if only the $-V_{OUT}$ is loaded. A 10% FL must be connected from $+V_{OUT}$ to Ground.
- ³ Contact factory for -55° to $+85^{\circ}$ C operating temperature range.
- ⁴ The maximum input current at any given input range measured at minimum input voltage is given as $1.6 \cdot I_{NOMINAL}$. Nominal input current is the typical value measured at the input of the converter under full-load room temperature and nominal input voltage (12, 24 and $48V_{IN}$).
- ⁵ Adequate insulation is to be provided to the converters at the end usage as per applicable requirements.
- ⁶ Temperature rise on the case of the converters is to be considered during the end usage as per applicable requirements.

ORDERING GUIDE

LNB15 / X-

Power _____

Number of Outputs (S = Single, D = Dual) _____

Output Voltage _____

Case Type: _____

Insert **A1** for 2:3 pin case without ON/OFF pin

Insert **A2** for 3:3 pin case with ON/OFF pin

Insert **X** for -55° C to $+85^{\circ}$ C (Optional)

Input Voltage _____

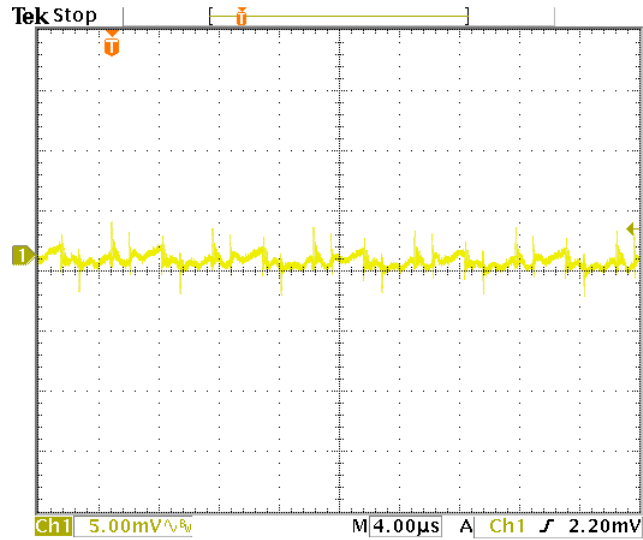
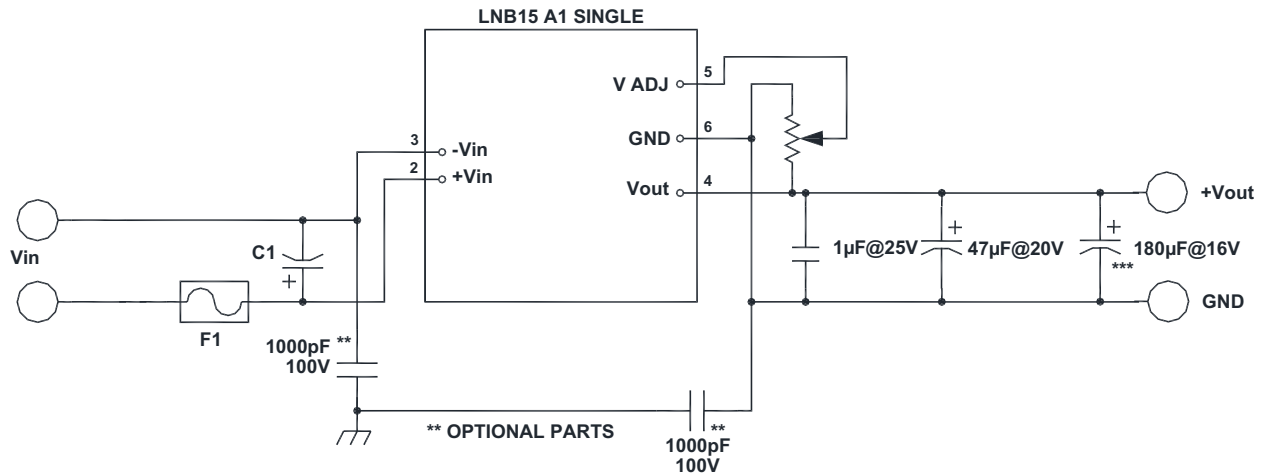


FIGURE 3. Typical output ripple of LNB15SS/24-A1 as shown in Figure 4 with an additional 180uF@16V low esr capacitor.

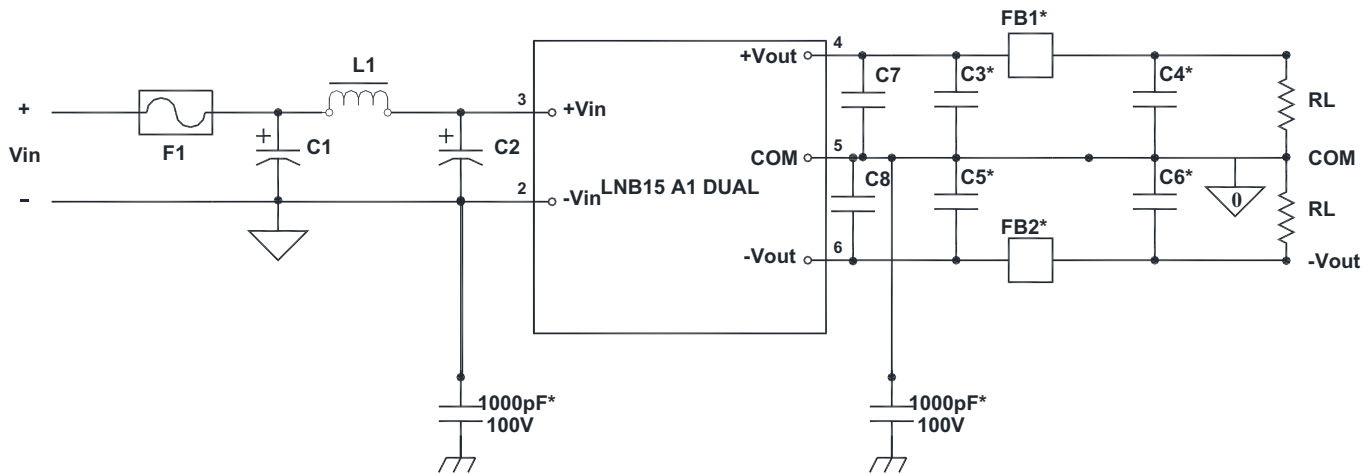


***OPTIONAL PART TO FURTHER REDUCE OUTPUT RIPPLE

Typical Values

V _{IN}	F1 (A)	C1 (Low ESR) (µF)	L1 (µH)	C2 (µF)	C Internal (µF)
12	2	47@25V	0.6	47@25V	33@25V
24	1	22@50V	1.0	22@50V	3.3@50V
48	0.5	10@100V	2.2	10@100V	2.2@100V

FIGURE 4. Typical connection diagram of LNB15 A1 Single DC/DC Converter



C7,C8 = 1µF@25V ceramic

FB1, FB2 = CMS2-5.6/3/4.8-4S2 (Ferroxcube)

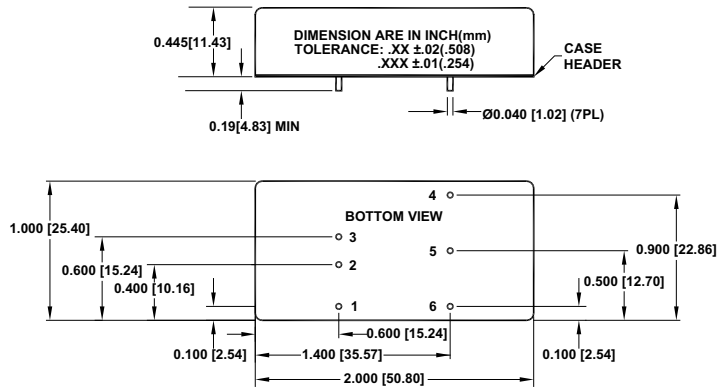
* Output filter component shown may be needed if output ripple/noise less than 10mV is required.

Typical Values

V _{IN}	F1 (A)	C1(Low ESR) (µF)	L1 (µH)	C2 (µF)	C3 - C6 (µF)
12	2	47@25V	0.6	47@25V	10
24	1	22@50V	1.0	22@50V	10
48	0.5	10@100V	2.2	10@100V	10

FIGURE 5. Typical connection diagram of LNB15 A1 Dual DC/DC Converter

**MECHANICAL SPECIFICATIONS
A1/A2 CASE**



A1/A2 CASE

Pin	Function	
	SINGLE	DUAL
1	ON/OFF	ON/OFF
2	-V _{IN}	-V _{IN}
3	+V _{IN}	+V _{IN}
4	+V _{OUT}	+V _{OUT}
5	V _{OUT} ADJ	GND (COM)
6	GND	-V _{OUT}

Note: On/Off pin only on A2 configuration