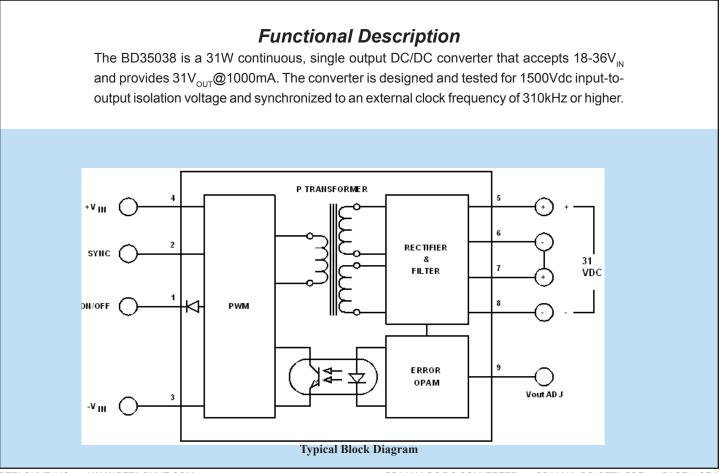


### **Key Features**

- 81% efficiency
- 2:1 input voltage range
- Input under/overvoltage protection
- 1500Vdc input-to-output isolation
- Soft start
- Dual short circuit protection
- 500µA off state current
- Multiple converter synchronization
- Adjustable outputs
- 300kHz switching frequency
- Thermal protection
- Dual isolated outputs
- Six-sided shielding



## BD35038 31W Single DC/DC Converter

18-36V<sub>IN</sub>, 31V<sub>OUT</sub> High Isolation



# Electrical Specifications

Unless otherwise specified, all parameters are given under typical +25°C with nominal input voltage and under full output load conditions.

UNIT Vdc

%

mΑ

 $%V_{PP} \text{ of } V_{OUT}$ 

%

%

%/°C

μS

%

PARAMETER	CONDITION / NOTE	MIN	ТҮР	MAX	UNIT
Input Voltage Range		18	24	36	Vdc
Input Startup Voltage		17			Vdc
Input Overvoltage Protection		37	38		Vdc
Input Filter	Ρί (π)				
Reverse Polarity	External series-blocking diode				
Reflected Ripple	I <sub>0</sub> = FL, C <sub>1</sub> = 100μF		50		mV <sub>PP</sub>
No Load Input Current			30		mA
Full Load Input Current			1610		mA
Input Surge Current (20µS Spike)				10	A
Short Circuit Current Limit	Will allow for 100W P <sub>o</sub>				
Off State Current			500		μA
Remote ON/OFF Control					
Supply ON	Pin 1 Open (Open circuit voltage: 13V max.)				
Supply OFF		0		0.8	Vdc
Logic Input Reference	-Input for ON/OFF and SYNC				
Logic Compatibility for Reference	TTL Open Collector or CMOS Open Drain				
Sync, High	See External Synchronization, Figure 4	2		6	Vdc
Sync, Low	See External Synchronization, Figure 4	0		0.8	Vdc

#### **OUTPUT SPECIFICATIONS** PARAMETER **CONDITION / NOTE** MIN TYP MAX Output Voltage Measured between Pin 5 (+) and Pin 8 (-) 31 **Output Voltage Accuracy** ±1 ±2 Output Current Continuous 1000 Ripple & Noise 1 2 Line Regulation, Single and Dual ±0.5 ±1 Load Regulation, Dual With balanced loads ±1 ±2 Temperature Coefficient @ FL 0.02 Transient Response Time 50% FL to FL to 50% FL 200 250 Short Circuit Protection By input current limiting Output Adjust Range ±5 ±10

#### **GENERAL SPECIFICATIONS**

PARAMETER	CONDITION / NOTE	MIN	ТҮР	MAX	UNIT
Efficiency			81		%
Isolation Voltage (1 min.), Input to Output			1500		Vdc
Isolation Resistance			10 <sup>9</sup>		Ω
Isolation Capacitance			2700		pF
Switching Frequency			300		kHz
Turn On Delay	See Figure 6		7	10	mS
Soft Start Time	See Figure 6		7	15	mS

#### **ENVIRONMENTAL SPECIFICATIONS**

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Operating Temperature Range (Ambient)*	See Figure 2	-40		+71	°C
Storage Temperature Range		-55		+125	°C
Thermal Resistance	°C per watt internally dissipated		7	10	°C/W <sub>DISS</sub>
Maximum Operating Case Temperature				110	°C
Derating	See Figure 2				
Cooling	Free-air convection				
EMI/RFI	Six-sided continuous shielded metal case				
MTBF	per MIL-HNBK-217F (Ground benign, +25°C)		1.1×10 <sup>6</sup>		hours
Humidity	Up to 95% non-condensing				
Thermal Shutdown	Case Temperature		110	115	°C
Thermal Hysterisis		2	5		°C

\* See footnotes 2, 3, 4 and 5

#### PHYSICAL CHARACTERISTICS

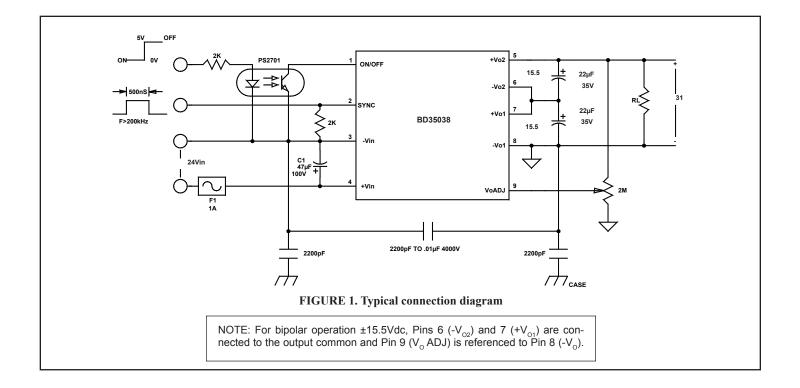
PARAMETER	CONDITION / NOTE	MIN	ТҮР	MAX	UNIT
Dimensions (L×W×H)	2.00×2.00×0.50 in. (50.80×50.80×12.70mm)				
Weight	2.43 oz. (69g)				
Case Material	Coated metal				
Shielding Connection	-V <sub>IN</sub> (Pin 3)				

<sup>1</sup> <u>MWARNING</u> - Usage of input fuse with adequate ratings is essential to avoid possible hazard and damage of the unit. A suppressor diode with adequate ratings is intended to be connected in series to the supply for reverse polarity protection.

<sup>2</sup> Contact factory for -55° to +85°C operating temperature range.

<sup>3</sup> Adequate insulation is to be provided to the converters at the end usage as per applicable requirements.

<sup>4</sup> Temperature rise on the case of the converters is to be considered during the end usage as per applicable requirements.



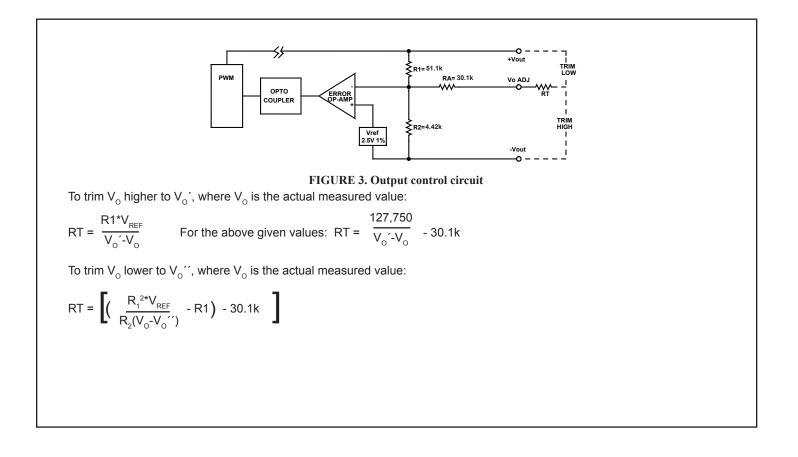
### EXTERNAL TRIMMING OF OUTPUT VOLTAGES (SINGLE AND DUAL ONLY)

To trim the output voltage DOWN, connect a 5%  $\frac{1}{4}$ W 1.5M $\Omega$ resistor between the +V<sub>01</sub> (Pin 7) output and trim pin of the converter. work which improves load regulation. If the load is some distance To trim the output voltage UP, connect a 5% 1/4W resistor between from the converter, the use of #20 gauge wire is recommended to the -V<sub>o1</sub> (Pin 8) output and trim pins of the converter. For UP/DOWN avoid excessive voltage drop due to the resistance of the circuit trimming capability, connect a  $50k\Omega$  potentiometer between the + and paths. - output pins, with the wiper arm connected to the trim pin.

The trim resistors/potentiometer can be connected at the con- DC-001: Testing Transient Response in DC/DC Converters verter output pins or the load. However, if connected at the load, DC-004: Thermal Consideration for DC/DC Converters

the resistance of the runs becomes part of the feedback net-

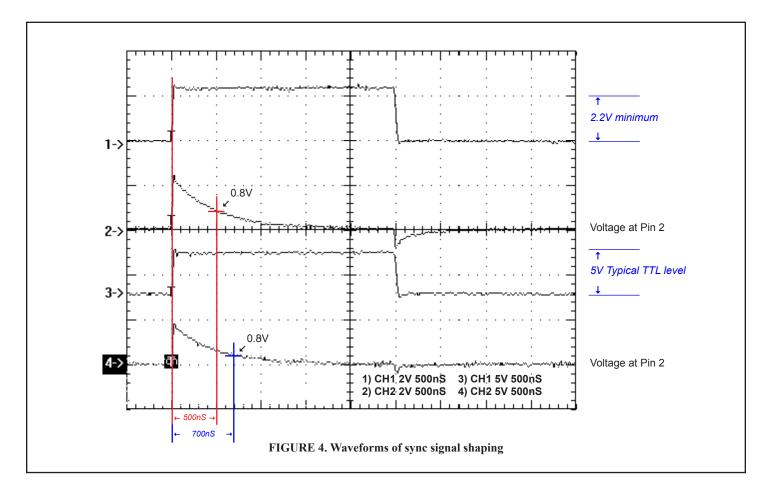
See our application notes:



### EXTERNAL SYNCHRONIZATION

A TTL signal applied at the SYNC pin of the converter will synchronize the switching frequency of the converter to that of the TTL input signal. The external (TTL) frequency must be equal or higher than the converter's frequency. At the positive-going edge of the applied pulse, the internal power-switching transistor turns off and the PWM discharges its timing capacitor. At the negative-going edge, the PWM resumes normal operation. The minimum positive pulse width of the TTL signal must be 300nS minimum and its frequency

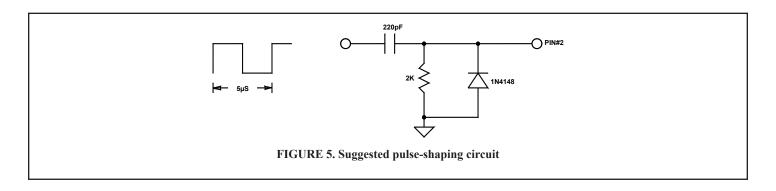
between 320kHz and 360kHz. NOTE: Higher frequencies will reduce the efficiency of the converter and wide TTL pulses will force the PWM to follow the external TTL width modulation, which may effect regulation. A high TTL signal at the SYNC pin of the converter will turn the converter off. An internal pull-down resistor will keep this pin low when it is not used. A pulse differentiator (see Figure 5) can be used to shape a square wave sync signal as shown in Figure 4.



#### SYNC SIGNAL SHAPING

As described in External Synchronization, the PWM of the converter requires a TTL signal of 0.8 to 2Vdc minimum amplitude and minimum duration of 300nS. When such a signal is not available (through one shot multivibrator or other pulse-shaping circuits) a C-R differentiator, such as the one in Figure 4, can be used to shape a square wave TTL signal. As is shown by the oscillogram in Figure

4, the positive edge of the sync pulse must be 2V minimum and the decaying exponential must reach the low 0.8Vdc in 300nS minimum from the positive edge. The parallel diode with the resistor is a small signal switching diode or a Schottky signal diode with 0.3 to 0.5V forward drop, it is used to clamp the voltage at pin 2@-0.5Vdc.



#### SHORT CIRCUIT PROTECTION

The converter has a dual short circuit protection feature. At the input side of the converter, two short circuit current comparators are used to monitor the input current of the converter. They are biased at different voltage levels; the lower threshold (LTH) comparator provides the power limiting function of the converter. Under normal operating conditions, the LTH comparator limits the output power of the converter when the maximum output power is exceeded.

When a hard short is applied across the output of the converter and the input current exceeds the set threshold of the second comparator, the converter goes into shutdown mode, the overcurrent latch is set and the converter is turned off. The converter will turn on again when its input voltage is recycled (OFF-ON) or if the ON/OFF pin is used to turn the converter on and off. The time required for the ON/OFF pin to be held low is between 100mS and 800mS.

#### **MECHANICAL SPECIFICATIONS**

