



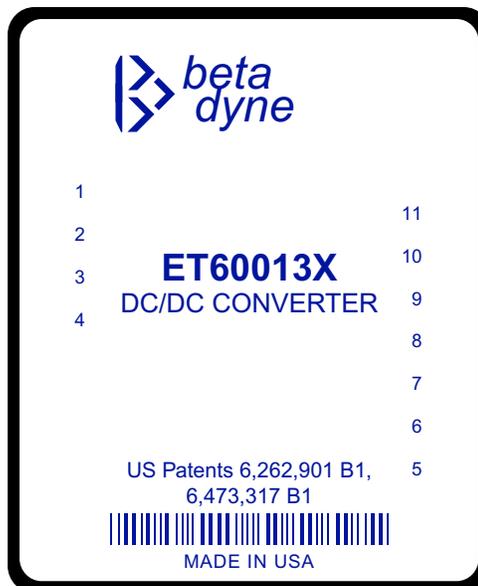
ET60013X

50W SINGLE DC/DC CONVERTER

14–36V_{IN}, 5V_{OUT}@10A
 US Patents 6,262,901 B1 & 6,473,317 B1

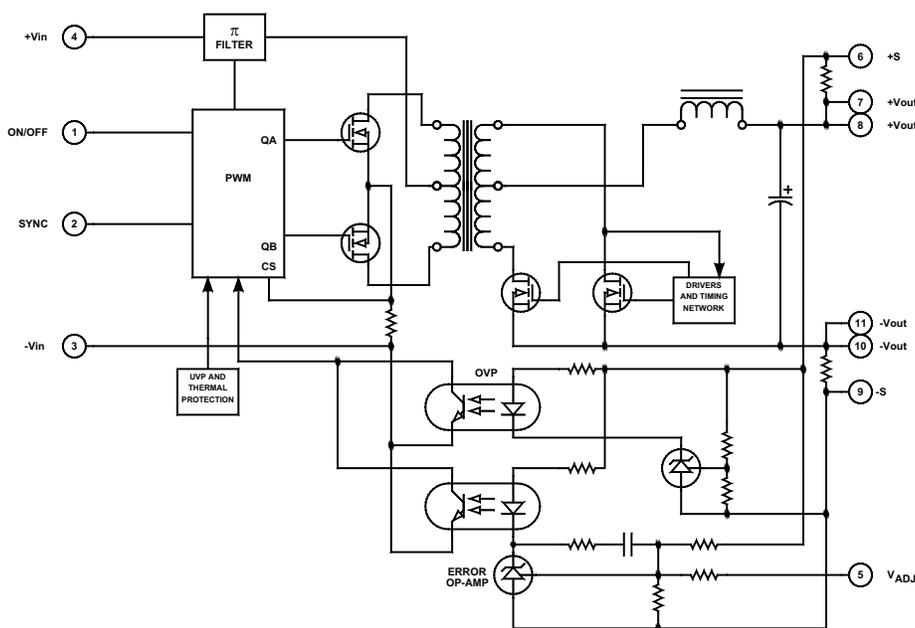
Key Features

- 89% efficiency
- Output overvoltage protection (OVP)
- Wide input voltage range (2:1)
- Six-sided shielding
- Soft start
- 1500Vdc input-to-output isolation
- Short circuit and thermal protection
- Adjustable output
- 400µA off state current
- Output synchronous rectification
- Input undervoltage protection
- Synchronize to $F_{EXT} \geq 310\text{kHz}$



Functional Description

The ET60013X is an isolated 50W single output DC/DC converter that accepts 14V_{IN} to 36V_{IN} and provides 5V_{OUT}@10A. Push-pull topology and output synchronous rectification allow for continuous operation even at low input voltages with maximum efficiency. Six-sided shielding with external synchronization minimizes EMI and RFI. Protection features allow the converters to operate in harsh environments over an extended temperature range of -30° to +85°C.



Typical Block Diagram

Electrical Specifications

INPUT SPECIFICATIONS

Unless otherwise specified, all parameters are given under typical +25°C with nominal input voltage and under full output load conditions.

PARAMETER	CONDITION/NOTE	MIN	TYP	MAX	UNIT
Input Voltage Range		14	24	36	V _{dc}
Input Filter	*				
Reverse Polarity Input Current	External anti-back-feeding diode			12	A
Input Surge Current (50µs @10s)				10	A
No-Load Input Current			50		mA
Full Load Input Current			2041		mA
Short Circuit Current Limit			150		% I _L
Undervoltage Short down		7			V _{dc}
Off-Load Current			400		µA
Remote ON/OFF Control					
Converter ON	Open (Open circuit voltage of Pin 1: 10V Max)				
Converter OFF		0.6	0	0.2	V _{dc}
Logic Input Reference	-Input				
Logic Compatibility	TTL Open Collector or CMOS Open Drain				
Sync Input	TTL	2.5		5	V _{dc}
Sync Input Frequency Range		310	320	330	kHz
Sync Input Minimum Pulse Width	See Figure 4	200			ns

OUTPUT SPECIFICATIONS

PARAMETER	CONDITION/NOTE	MIN	TYP	MAX	UNIT
Output Voltage			5		V _{dc}
Output Current			10		A
Output Voltage Accuracy			±1	±1.5	%
Output Voltage Adjustment			3	±5	%
Minimum Load		10			% of I _L
Ripple & Noise			1	2	$\frac{V_{pp}}{V_o}$
Line Regulation	Minimum V _{in} to maximum V _{in}		±1	±2	%
Load Regulation	HL to FL		±1	±2	%
Temperature Coefficient @ FL			0.02		%/°C
Transient Response Time (to within 1% of V _{out})	50% FL to FL to 50% FL, See Figure 3		50		µs
Short Circuit Protection	By input current limiting				
Output Short Circuit Duration	Continuous				

GENERAL SPECIFICATIONS

PARAMETER	CONDITION/NOTE	MIN	TYP	MAX	UNIT
Efficiency			79		%
Lock-on Voltage (1 min)			1500		V _{dc}
Lock-on Resistance			10 ⁴		Ω
Lock-on Capacitance			30		µF
Switching Frequency			150		kHz

ENVIRONMENTAL SPECIFICATIONS

PARAMETER	CONDITION/NOTE	MIN	TYP	MAX	UNIT
Operating Temperature, Industrial (unlabeled)	See Figure 2	-30		+75	°C
Storage Temperature Range		-55		+125	°C
Thermal Resistance			2.5		°C/W _{amb}
Minimum Operating Case Temperature			75		°C
Thermal Turn-Off, Case Temperature		70	90	100	°C
Thermal Hysteresis			10		°C
Dewling	See Figure 2				
Humidity	Up to 95% non-condensing				
Cooling	Free air convection				
EMI/EMF	Shielded continuous air-aided metal case				
WTEF	per MIL-PRC-217F (Ground benign, +25°C)		625,000		hour

PHYSICAL CHARACTERISTICS

PARAMETER	CONDITION/NOTE	MIN	TYP	MAX	UNIT
Dimension (L*W*H)	2.00±0.00±0.50 In. (50.80±0.50±12.70mm)				
Weight	±2% oz. (7.2g)				
Case Material	Oxided metal				
Shielding Connection	+V _{in} (Pin4)				

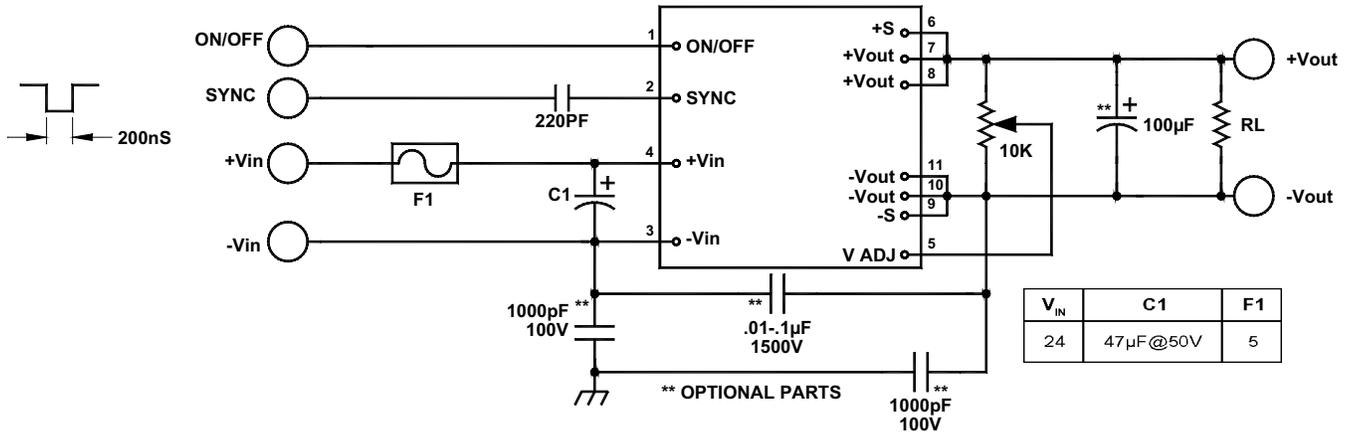


FIGURE 1. Typical connection diagram

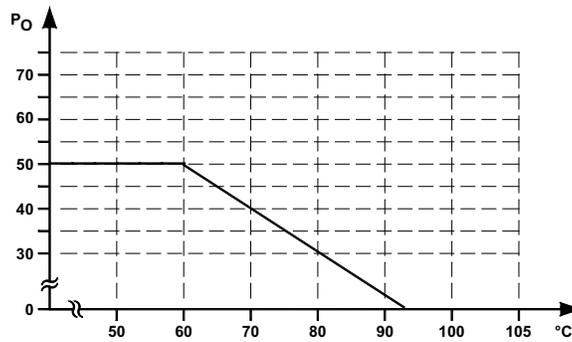


FIGURE 2. Typical derating curve

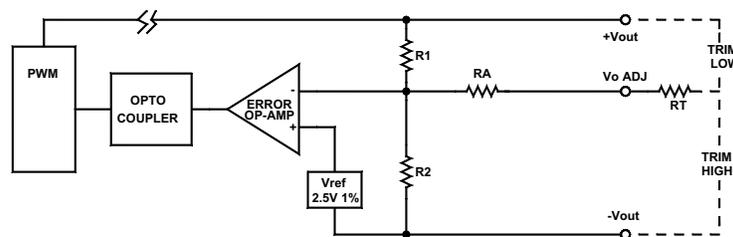


FIGURE 3. Output control circuit

To trim V_o higher to V_o' , where V_o is the actual measured value:

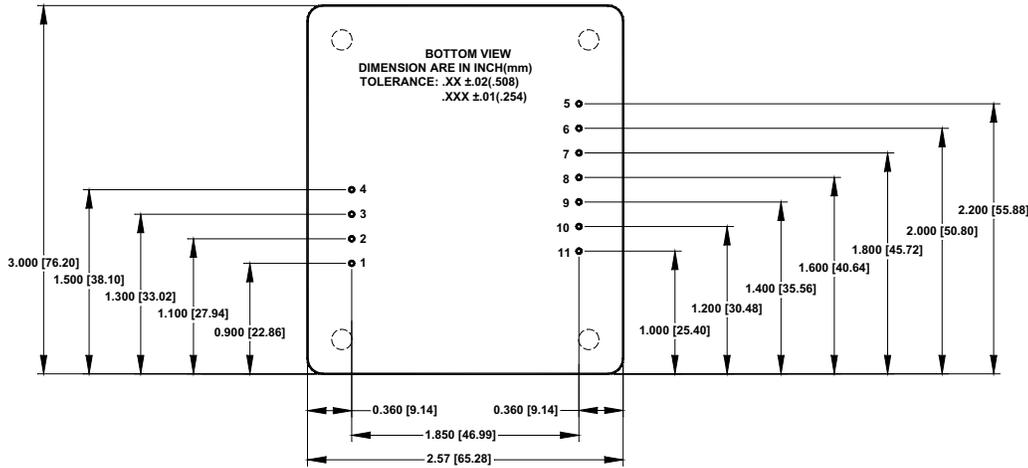
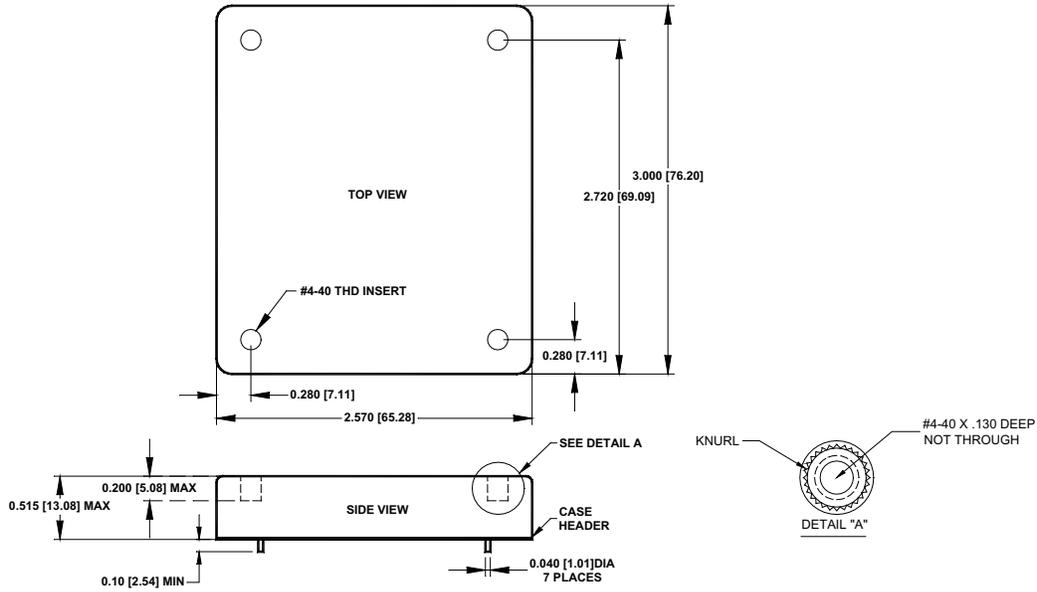
$$RT = \frac{R1 \cdot V_{REF}}{V_o' - V_o}$$

To trim V_o lower to V_o'' , where V_o is the actual measured value:

$$RT = \left[\left(\frac{R1 \cdot V_{REF}}{R2(V_o - V_o'')} - R1 \right) - RA \right]$$

Resistor	Value (KΩ)
R1	2.49
R2	2.49
RA	20

MECHANICAL SPECIFICATIONS



Pin	Function
	SINGLE
1	ON/OFF
2	SYNC
3	-V _N
4	+V _N
5	V _{OUT} ADJ
6	+SENSE
7	+V _{OUT}
8	+V _{OUT}
9	-SENSE
10	-V _{OUT}
11	-V _{OUT}

EXTERNAL SYNCHRONIZATION

The converter can be synchronized to an external clock by driving the SYNC pin (pin 2) directly. The driving signal frequency must be 330kHz \pm 5% (200ns to 300ns negative going pulse (See Figure 4, Waveform 1)). When the external clock is AC-coupled to the SYNC pin of the converter through a ceramic capacitor, connect

a signal Schotky diode with the cathode connected to the SYNC pin and the anode to $-V_{IN}$ (See Figure 5). AC coupling reduces the power required for driving multiple converters and allows for continuous operation of the other synchronized converters in case the driving signal is missing or a short circuit develops at one of the sync inputs.

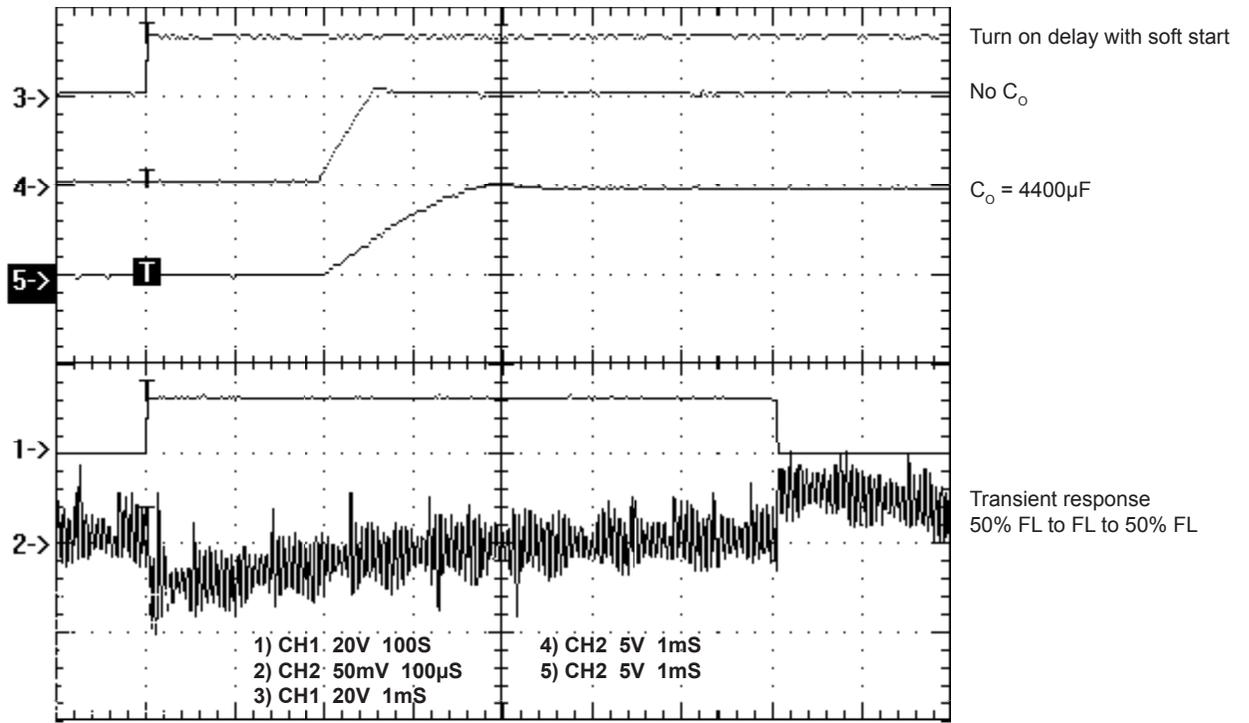


FIGURE 4. Turn on delay with soft start and transient response

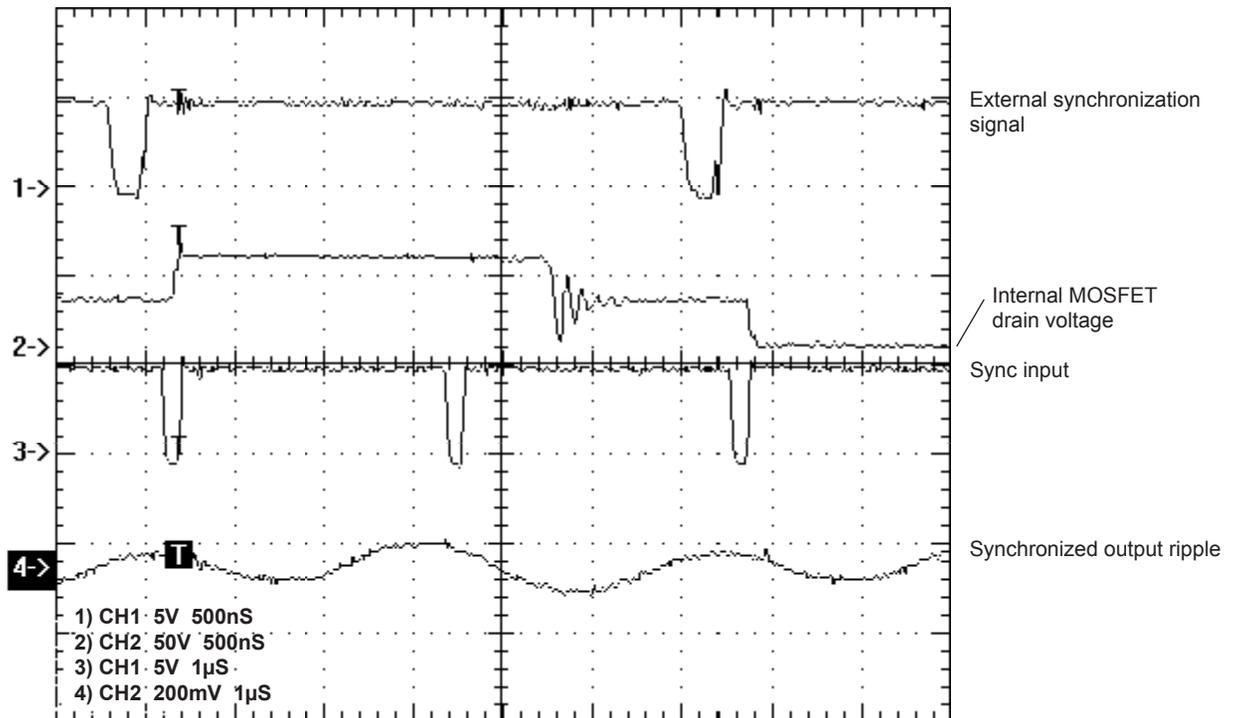


FIGURE 5. Synchronization waveforms