



Q25007
30W DC/DC CONVERTER
 22-26Vin, +/-24Vout@+/-313mA

Key Features

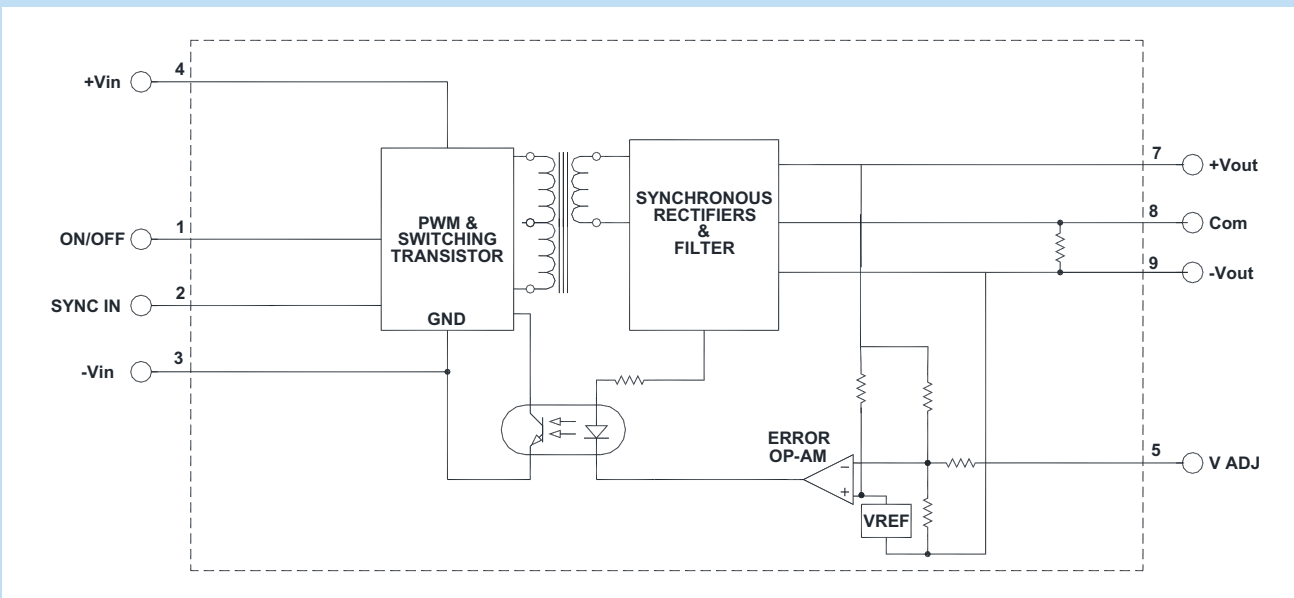
- Efficiency 88%
- Low output noise
- Six-sided shielding
- Input-to-output isolation
- Soft start
- External synchronization
- Short circuit protection
- Thermal protection
- Industry standard pinout



Beta Dyne is protected under various patents, including but not limited to U.S. Patent numbers: 5,777,519; 6,188,276; 6,262,901; 6,452,818; 6,473,3171.

Functional Description

The Q25007 is a 22-26V input range DC/DC converter that provides +/-24Vout @ +/-313mA. The converter is designed to synchronize to an external clock of 400kHz.



Typical Block Diagram

Unless otherwise specified, all parameters are given under typical ambient temperature of +25°C with an airflow rate = 400LFM. With the given power derating, the operating range is -40°C to +125°C. Specifications subject to change without notice.

Electrical Specifications

INPUT SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Input Voltage Range		22	24	26	Vdc
Input Startup Voltage		20			Vdc
Input Overvoltage Shutdown $24 V_{IN}$		27			Vdc
Input Filter	C				
No Load Input Current			23.5		mA
Full Load Input Current			706		mA
Input Surge Current (20 μ S Spike)				10	A
Short Circuit Current Limit	120% Of I_{IN} @ Full Load				
Off State Current			2.5		mA
Remote ON/OFF Control					
Supply ON	Pin 3 Open (Open circuit voltage: 10V Max.)				
Supply OFF		0		0.6	Vdc
Logic Input Reference	-Vin				
Logic Compatability	TTL Open Collector or CMOS Open Drain				

OUTPUT SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Voltage			+/-24		Vdc
Output Voltage Accuracy	With balanced loads		1	1.5	%
Output Current			+/-313		mA
Output Voltage Adjustment			± 5	± 10	%
Ripple & Noise	For further reduction see Figure 1		40		mV
Line Regulation	Minimum V_{IN} to maximum V_{IN}		± 1		%
Load Regulation	10% to FL, with balanced loads		± 1		%
Temperature Coefficient @ FL			.01	.02	%/°C
Transient Response Time	50% FL to FL to 50% FL, within 1% of Vo, See Figure 6	100			μ S
Short Circuit Protection	By Hiccup Technique				
Output Overvoltage Protection	None				

GENERAL SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Efficiency (at full power)			88		%
Isolation Voltage (1 min.), Input to Output			1500		Vdc
Isolation Resistance			10^9		Ω
Isolation Capacitance			300		pF
Switching Frequency (FC)			375		kHz
External Sync Frequency (Fe)	See Figure 7 & 8		400		kHz

PHYSICAL CHARACTERISTICS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Dimensions (L×W×H)	2.00×1.00×0.450 in. (50.80×25.40×11.43mm)				
Weight	1.3 oz. (37g)				

ENVIRONMENTAL SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Operating Temperature Range (Ambient)	Industrial, See Note 2	-40		+71	°C
Storage Temperature Range		-55		+125	°C
Maximum Operating Case Temperature				110	
MTBF	Per MIL-HNBK-217F(Ground benign, +25 °C)		1.1×10^6		hours
Shielding Connection	-Vin for 24Vin				

¹ When converter enters thermal protection mode, its duty cycle is reduced momentarily and will resume after its internal temperature (pwm) drops down a few degrees (°C).The converter's output behaves similar to hiccup short circuit mode.

² Contact factory for -55° to +85°C operating temperature range.

³ The maximum input current at any given input range measured at minimum input voltage is given as $1.6 \cdot I_{\text{NOMINAL}}$. Nominal input current is the typical value measured at the input of the converter under full-load room temperature and nominal input voltage ($24V_{\text{IN}}$).

⁴ Adequate insulation is to be provided to the converters at the end usage as per applicable requirements.

⁵ Temperature rise on the case of the converters is to be considered during the end usage as per applicable requirements. To maintain a less than 110°C case temperature, you need 200LFM.

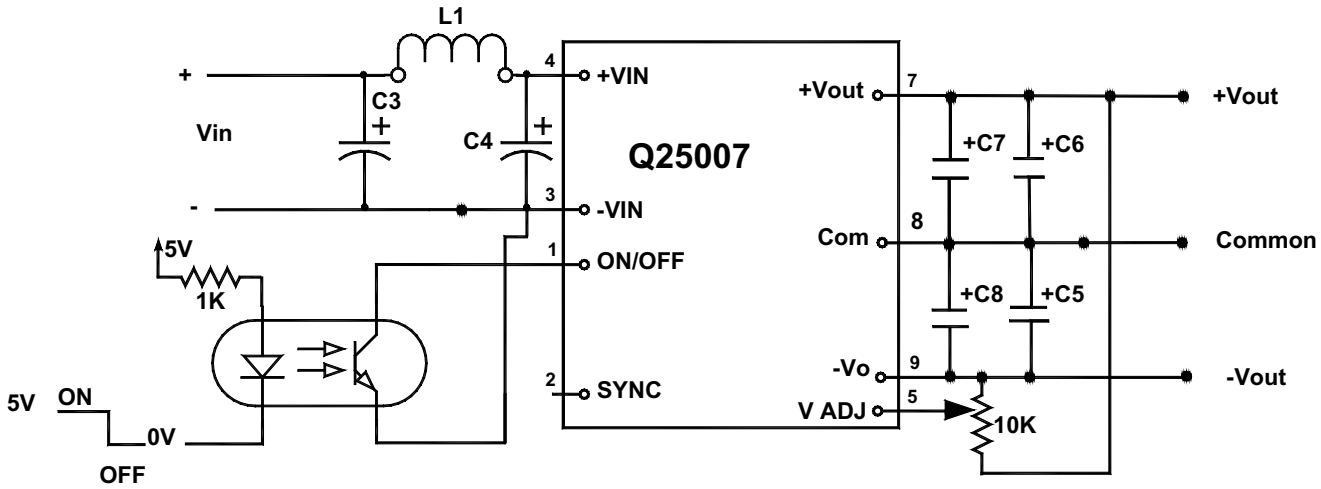


FIGURE 1. Typical connection diagram for Q25007

Part list for Figure 1:

L1= 2.2μH

C3= 50μF@100V Electrolytic

C4=50μF@100V Electrolytic

C7,C8= 22μF@35V Low ESR Tantalum Capacitors

C5,C6=22μF@35V Low ESR Tantalum Capacitors

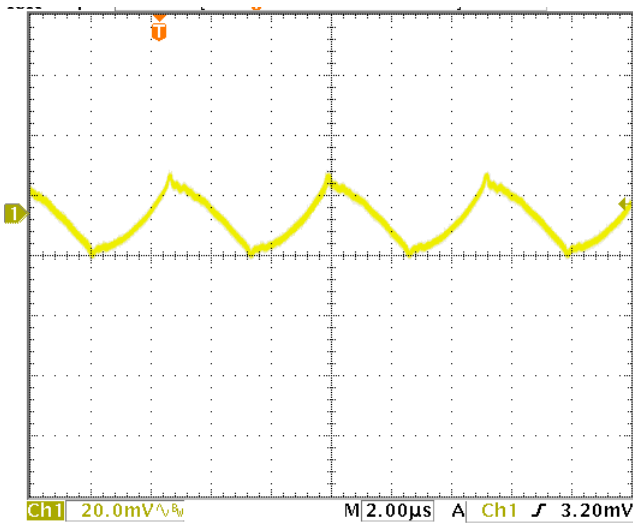


FIGURE 2: Output ripple of Q25007 on +Vout as shown in Figure 1, with no external output capacitors.

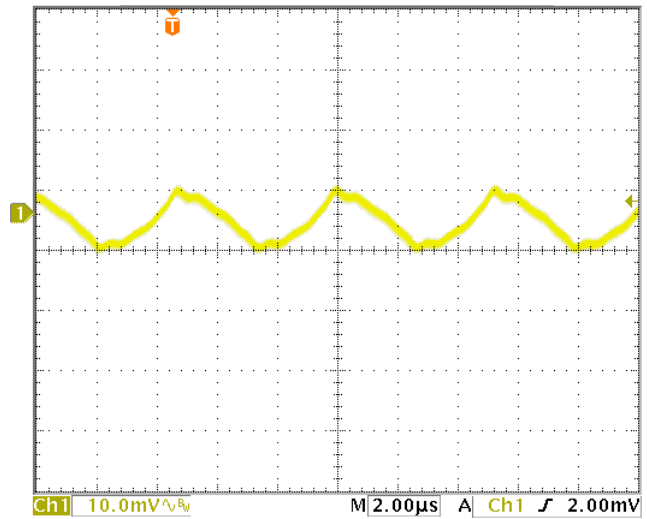


FIGURE 3: Output ripple of Q25007 on +Vout with external cap of two parallel 22μF@35V low ESR capacitors.

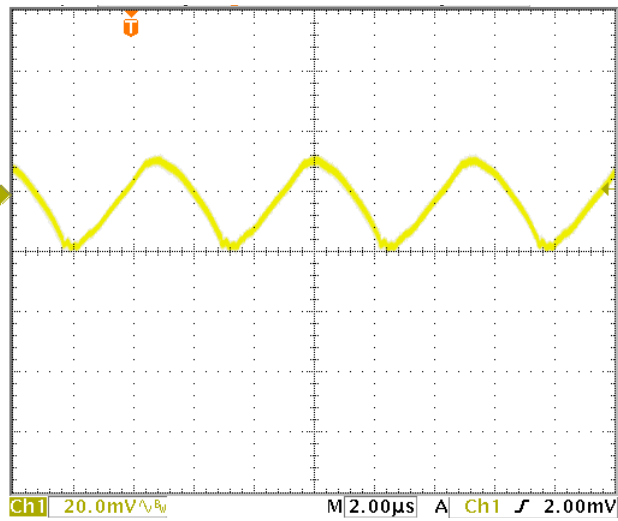


FIGURE 4: Output ripple of Q25007 on +Vout as shown in Figure 1, with no external output capacitors.

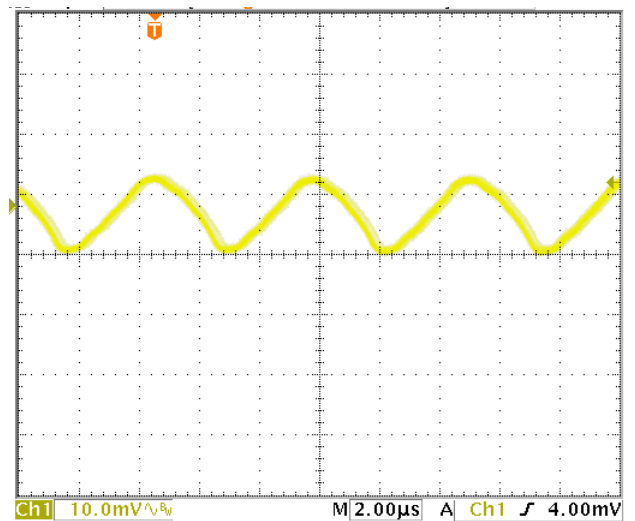


FIGURE 5: Output ripple of Q25007 on +Vout with external cap of two parallel 22µF@35V low ESR capacitors.

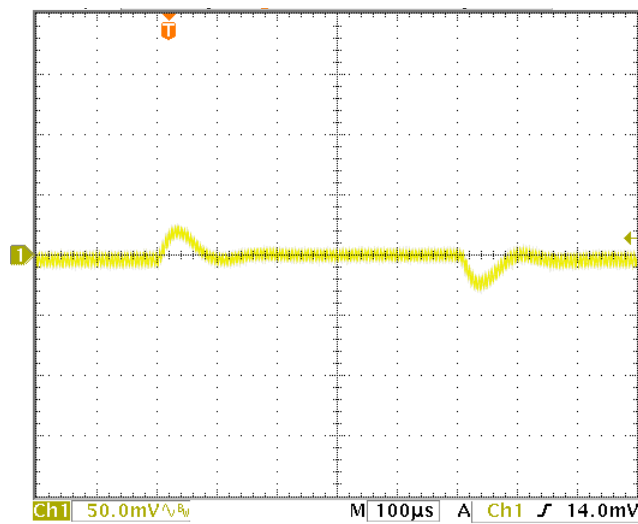


FIGURE 6. Transient Response at Vin=24, Iout changing from Full load to Half load on a Q25007.

EXTERNAL SYNCHRONIZATION

The SYNC pin can be used to synchronize the internal oscillator to external clock. An open drain output is the recommended interface between the external clock to the Q25 SYNC pin as shown in figure 7. The clock pulse width must be greater than 15ns. The external clock frequency must be greater than the frequency of the Q25.

Multiple Q25 converters can be synchronized together simply by connecting the converters SYNC pins together as shown in figure 8. Care should be taken to ensure the ground potential differences between the converters are minimized. In this configuration all the converters will be synchronized to the highest frequency device. The SYNC pin is a CMOS buffer with pull-up current limited to 200micro amps. If the external device forces the SYNC pin low before the internal oscillator ramp completes its charging cycle, the ramp will reset and another cycle begins. If the SYNC pins of multiple Q25 converters are connected together, the first SYNC pin that pulls low will reset the oscillator ramp of all the other converters. All converters will operate in phase when synchronized using the SYNC feature. Up to five devices can be synchronized using this method.

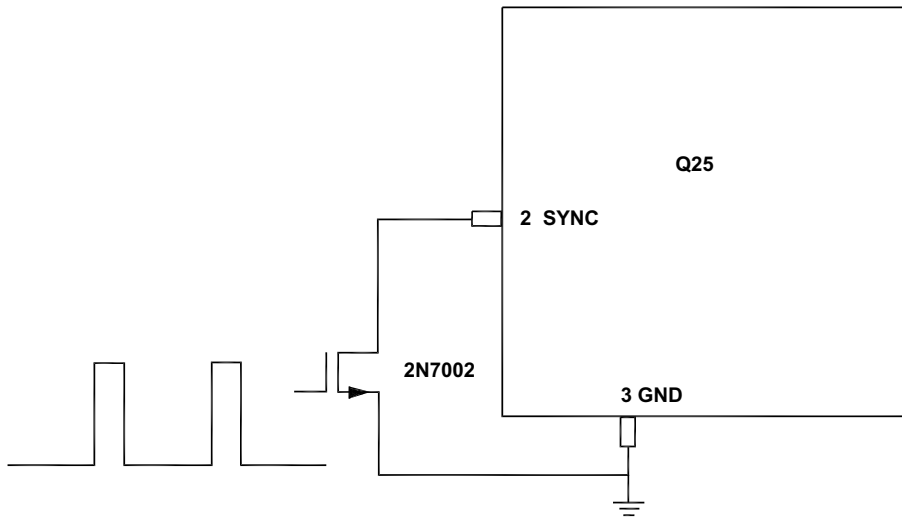


FIGURE 7. SYNC from external clock

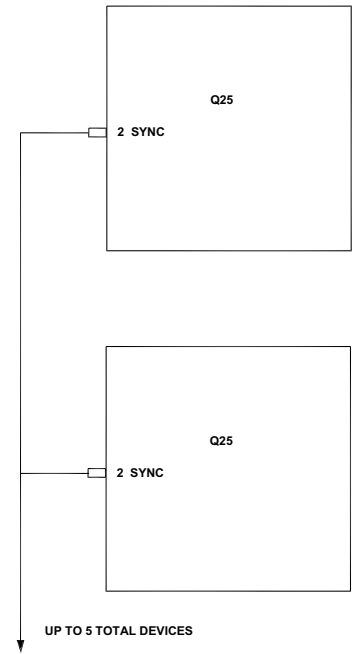
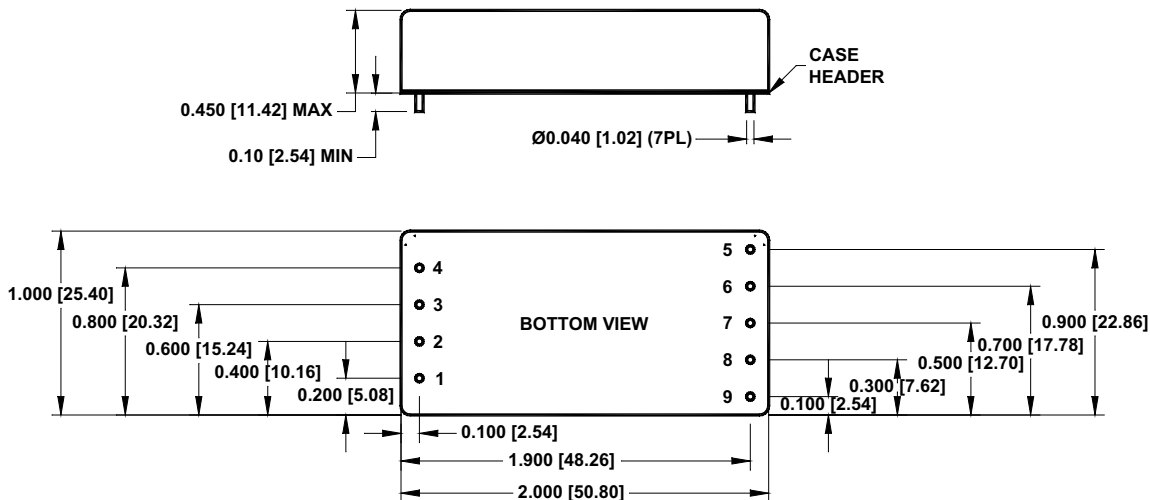


FIGURE 8. SYNC of multiple devices

DIMENSION ARE IN INCH(mm)
TOLERANCE: .XX ±.02(.508)
.XXX ±.01(.254)

MECHANICAL SPECIFICATIONS



Pin	Function
DUAL	
INPUT	
1	ON/OFF
2	SYNC IN
3	-V _{IN}
4	+V _{IN}
OUTPUT	
5	V _{ADJ}
6	No Pin
7	+V _{OUT}
8	Common
9	-V _{OUT}