SRAD20

20W Adjustable Step-Up Switching Regulator

Adjustable output from V_{IN} to 200V_{OUT}

Key Features

- Efficiency up to 88%
- Wide input range (5–18V)
- Input under/overvoltage protection
- Output overvoltage protection
- 150kHz constant frequency
- 50W/in^3 power density
- Six-sided shielding
- Thermal protection
- External synchronization

Beta Dyne is protected under various patents, including but not limited to U.S. Patent numbers: 5,777,519; 6,188,276; 6,262,901; 6,452,818; 6,473,3171.

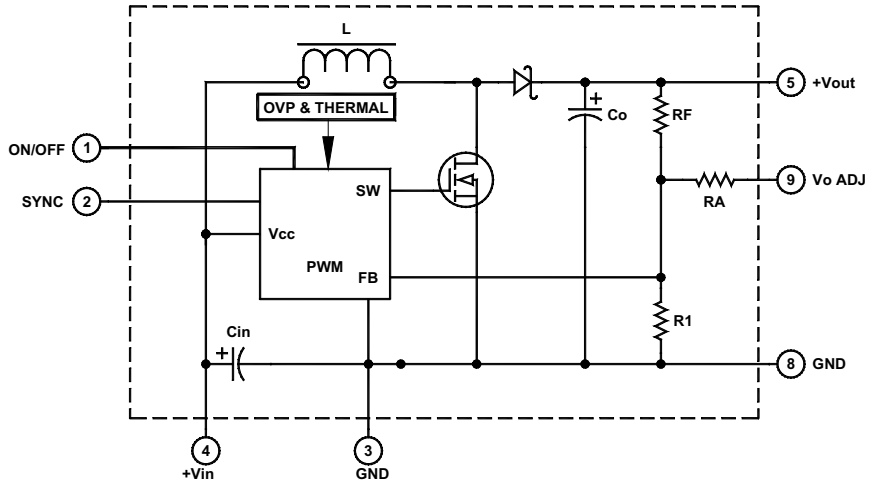
Applications

- Battery Chargers/Backup
- External Modems
- Programmable Power Supply
- xDSL Power Supply
- Telecom Hardware
- LAN and Network Systems
- Point of Sale (POS) Systems

Functional Description

The SRAD20 is a family of 20W constant frequency, current mode step-up switching regulators with excellent line and load regulation that accepts an input voltage from 5V_{IN} to 18V_{IN} and provides an adjustable output voltage from V_{IN} to 200V_{OUT}. High switching frequency and SMD technology makes achieving high power density, low cost and high reliability possible. The converters require a low impedance power source or minimum 1000µF input capacitor for proper operation. The converters come in a 2×1×0.39-inch package size. NOTE: Please see Application Notes SR-003 and SR-004.

NOTE: These converters DO NOT feature short circuit protection, you must use an external fuse to provide short circuit protection.



Typical Block Diagram

Electrical Specifications

Unless otherwise specified, all parameters are given under typical +25°C with nominal input voltage and under full output load conditions.

INPUT SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Input Voltage Range	See Model Selection Guide				
Input Current	NL, See Model Selection Guide				mA
Input Reflected Ripple	With 1000µF, See Figure 4		20		mA_{PP}
Turn On Delay	Including Soft Start, See Figure 3		5	8	mS
Overvoltage Lockout, 5V		9	10		Vdc
Overvoltage Lockout, 10V		18	19		Vdc
Undervoltage Lockout, 5V		4	4.5		Vdc
Undervoltage Lockout, 10V		8			Vdc
Remote ON/OFF Control, SYNC					
Logic Input Reference	-Input for ON/OFF and SYNC				
Logic Compatibility	TTL Open Collector or CMOS Open Drain				
SYNC Frequency		170	180	200	kHz
Converter ON	Open (Open circuit voltage at Pin 1: 10V Max.)				
Converter OFF	Pin 1 < 0.6V, V ADJ 2V				

OUTPUT SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Output Voltage	See Model Selection Guide	V_{IN}		200	Vdc
Output Voltage Accuracy			0.5	1	%
Output Current	See Figure 7	100		1000	mA
Line Regulation			±1	±2	% of V_{OUT}
Load Regulation			±1	±2	% of V_{OUT}
Ripple and Noise	See Figure 4		1	2	$_{OUTPP}$
Temperature Coefficient			0.01	0.02	%
Transient Response	See Figure 1		5		mS
Short Circuit Protection	None, Input Fuse is required				
V Adjust Range	V ADJ = 2V to 0V	V_{IN}		200	Vdc
Output Overvoltage Limit		200	210	220	Vdc

GENERAL SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Efficiency	See Model Selection Guide				
Switching Frequency	Fixed	150		170	kHz
Isolation	None				
Thermal Resistance			5		°C/W
Thermal Hysterisis			10		°C
Thermal Turn Off Temperature[2]	Case Temperature	80	90	95	°C
MTBF	per MIL-HNBK-217F (Ground benign, +25°C)		1.9×10^6		hours

ENVIRONMENTAL SPECIFICATIONS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Humidity	Non-condensing			95	%
Storage Temperature		-55		+125	°C
Operating Temperature, Commercial		-40		+70	°C

PHYSICAL CHARACTERISTICS

PARAMETER	CONDITION / NOTE	MIN	TYP	MAX	UNIT
Dimensions (L×W×H)	2.00×1.00×0.39 in. (50.80×25.40×9.90mm)				
Weight	1.09 oz. (31g)				
Shielding Connection	-V/-V, Pin 3				

[1] The off state output voltage is equal to V_{IN} minus 1 diode drop.
[2] After thermal turn off, $V_{OUT} \cong V_{IN}$ - 0.7V.

Model Selection Guide

MODEL NUMBER	INPUT					OUTPUT		
	Voltage (Vdc)		Current (mA)		Reflected Ripple[2] (mA_{PP})	Voltage (Vdc)	Current (mA)	Efficiency Full Load (%)
	Nominal	Range	No Load	Full Load				
SRAD20S200/5	7	6-10	50	3284	100	V_{IN}-200	100	87
SRAD20S200/12	12	9-18	30	1894	50	V_{IN}-200	100	88

[3] Measured with 1000µF input capacitor. See C_{IN} in Figure 6.
[4] For output power of 16W, this model will operate down to $5V_{IN}$.

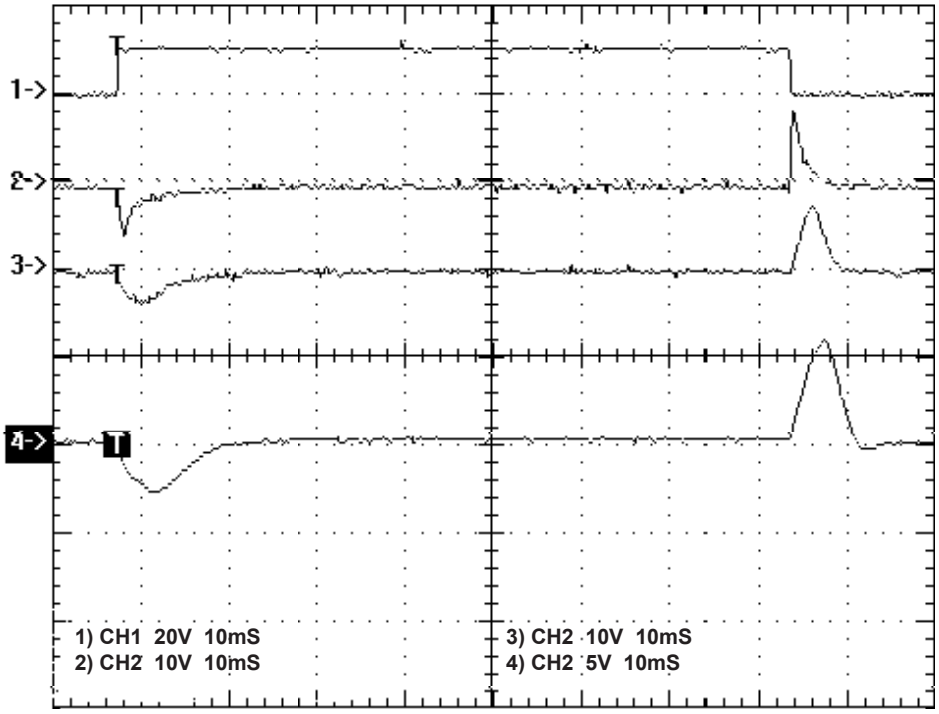


FIGURE 1. Transient response

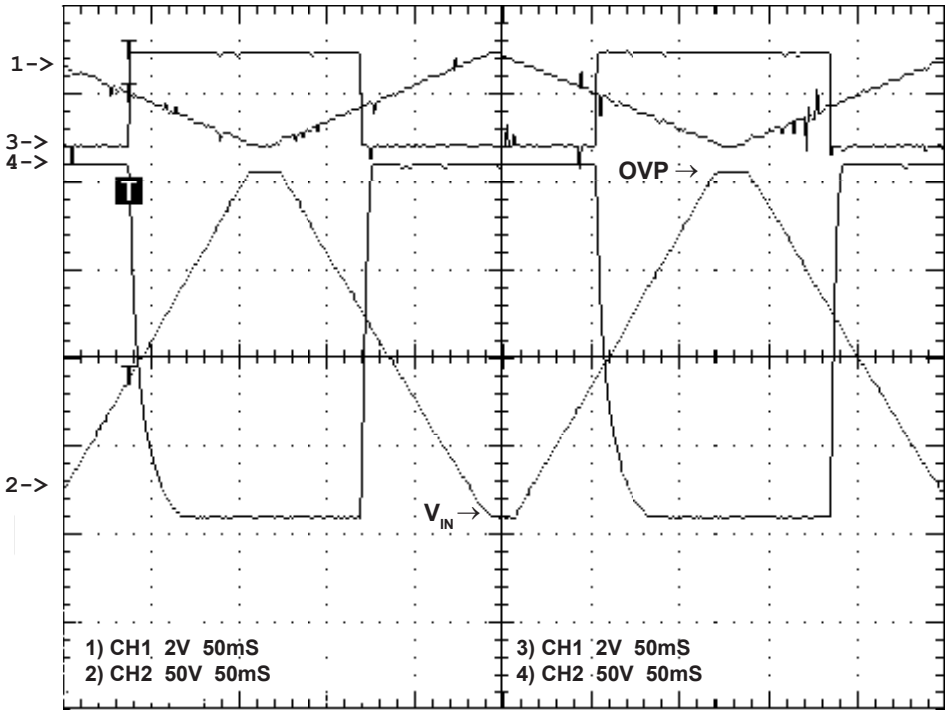


FIGURE 2. V_{IN} vs. V_{OUT} Linearity and Output voltage protection

Figure 2 shows the linearity of the converter. The converter can be adjusted from V_{IN} to +200Vdc. Also note the negative-going edge of waveform #4 is the R_L*C_O (R_L = 2K, C_O = 1µF External + 3µF Internal) time constant. During this time, the PWM is Off.

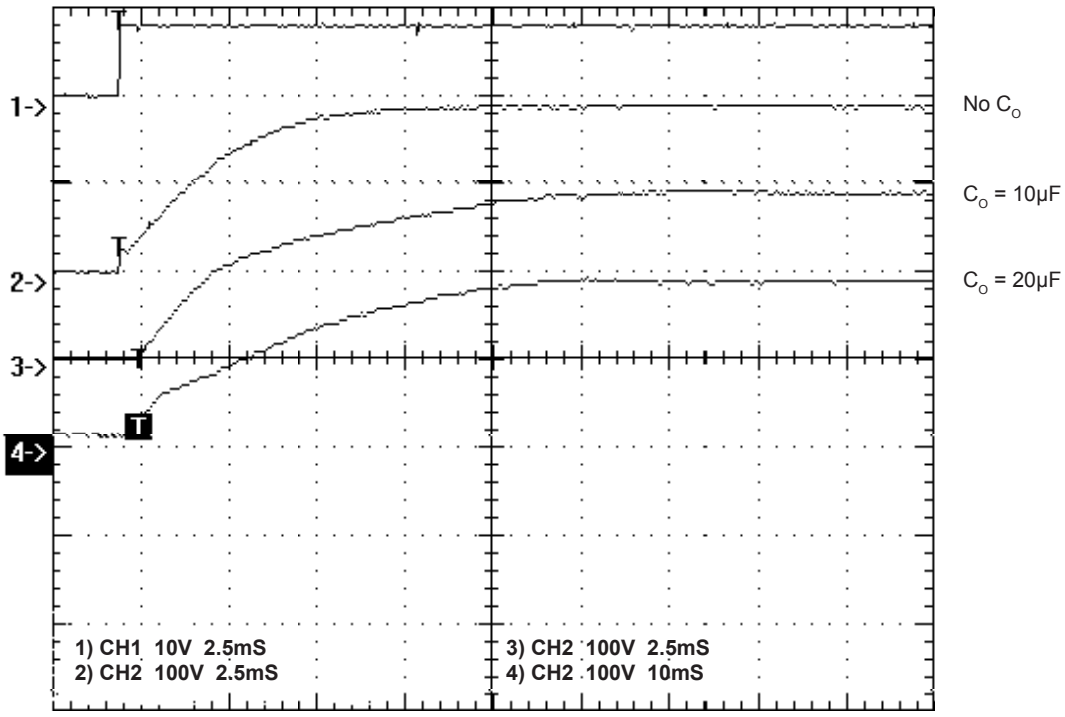


FIGURE 3. Turn on delay with soft start
(No additional C_O, C_O = 10µF, C_O = 20µF)

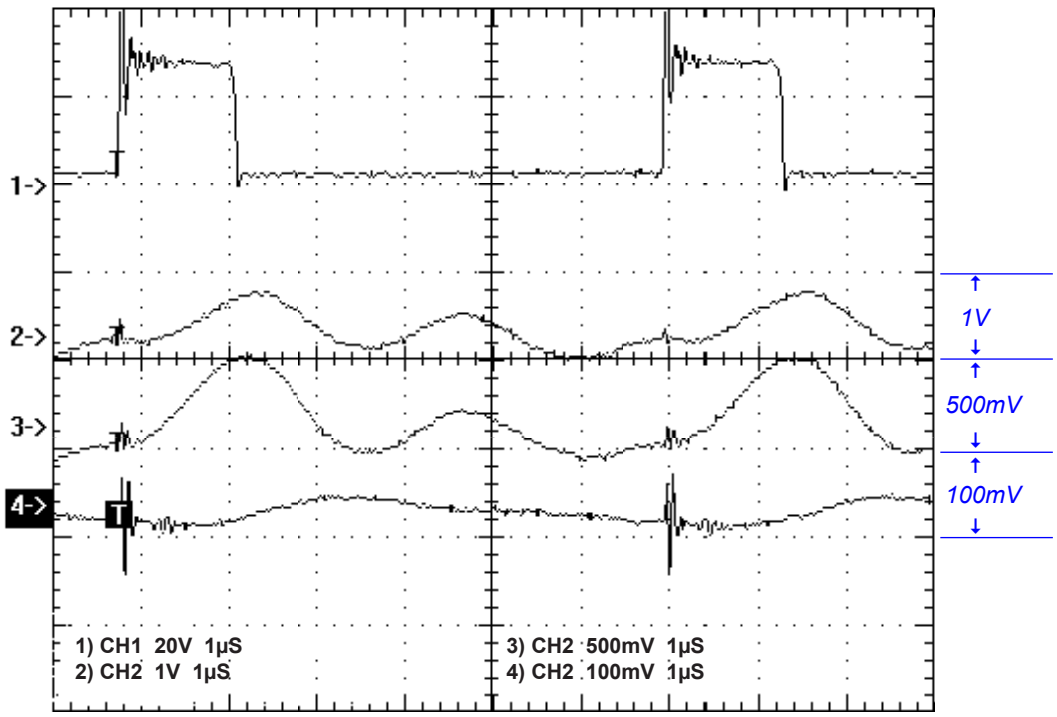


FIGURE 4. Output ripple of SRAD20S200/5

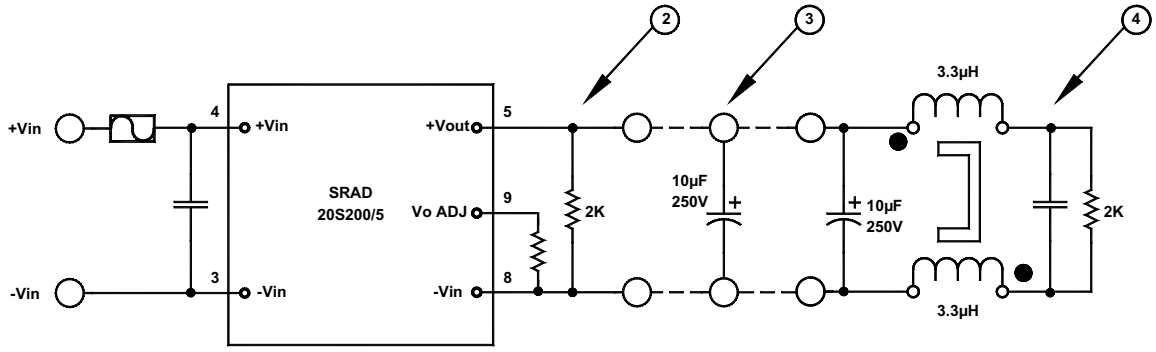


FIGURE 5. Circuit components used to obtain waveform in Figure 4 (See Figure 4)

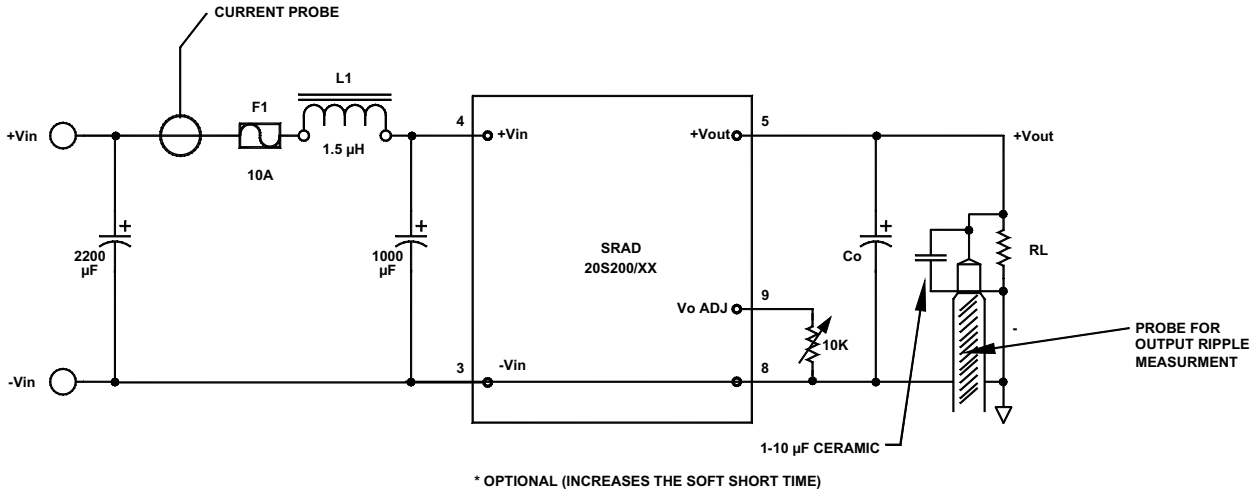


FIGURE 6. Connection diagram for input reflected ripple and output voltage ripple measurements

V_{IN}	F1
5V	8A Slow Blow
12V	4A Slow Blow

Adjusting V_O by R_X in KΩ: $V_O \cong 2\left(1 + \frac{511}{4.99+R_X}\right)$ $R_X = \frac{511}{(V_O/2) - 1} - 4.99$

Adjusting V_O by an external voltage source: $V_O \cong 206.8 - 102.4*V_{IN}$ or $V_{IN} = (206.8-V_O)/102.4$

V_o	Calculated R_x in K□	Nearest STD 1% in K□
20	51.78	52.30
40	21.90	22.10
60	12.63	12.70
80	8.11	8.06
100	5.44	5.49
120	3.67	3.65
140	2.42	2.43
160	1.48	1.47
180	0.75	0.75
200	0.172	0.174

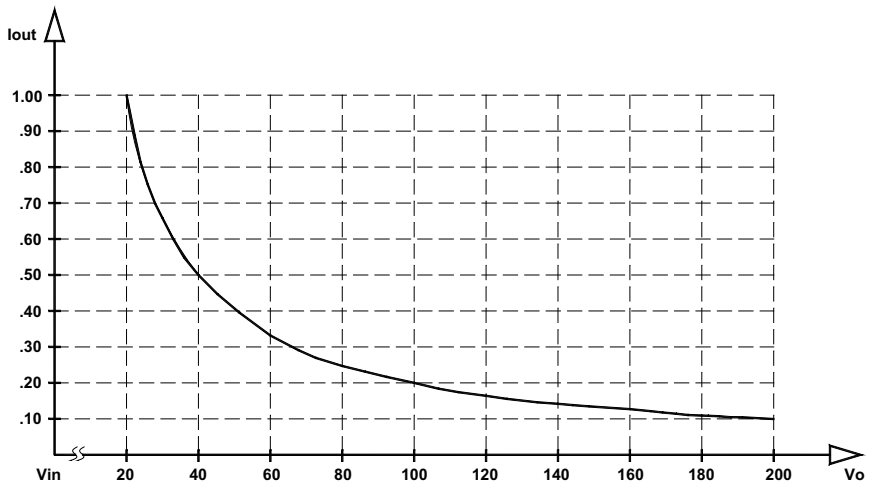
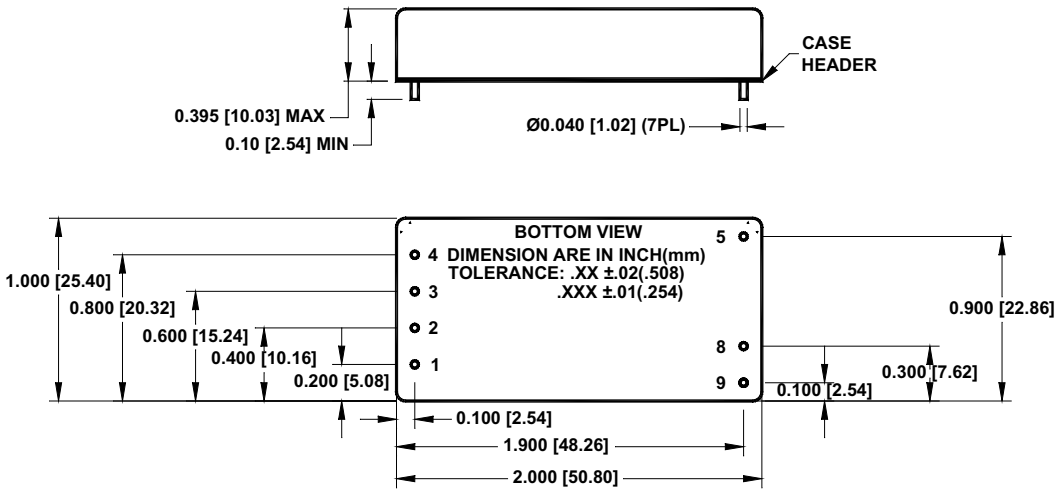


FIGURE 7. I_{OUT} vs. V_{OUT} for P_O Max ≤ 20W

MECHANICAL SPECIFICATIONS



Pin	Function
1	ON/OFF
2	SYNC
3	$-V_{IN}$ (INPUT GND)*
4	$+V_{IN}$
5	$+V_{OUT}$
6	No Pin
7	No Pin
8	GND*

* Input Ground (Pin 3) and Output Ground (Pin 8) are internally connected